PATENT TESSERA 3.0-078 DIV

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of Fjelstad et al.

Application No. 09/020,647

Filed: February 9, 1998

For: Methods of Making Compliant Semiconductor Chip Packages

(As Amended)

Box CPA

Commissioner for Patents Washington, D.C. 20231

INADEMARK OFFICE # 26/1

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Group Art Unit: 2814 Junle

Examiner: D. Graybill

Date: October 1, 2001

PRELIMINARY AMENDMENT

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Preliminary to initiation of continued prosecution of the above-identified application, please amend the application as follows:

IN THE TITLE

Please amend the title of the above-identified application --Methods of Making Compliant Semiconductor Chip Packagesto:

IN THE CLAIMS

Please cancel claims 1-11 and 21-34.

Insert new claims 35-57 as follows:

(New) A method of making a compliant semiconductor 35. chip package comprising:

providing a semiconductor chip having a contact bearing surface including a central region bounded by a peripheral region, wherein the peripheral region of said contact bearing surface has chip contacts;